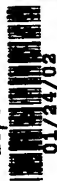


3C978 U.S. PTO

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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10053712	01/24/2002	438	503	2812	

****APPLICANTS:** Yamauchi Tsunenori;

****CONTINUING DATA VERIFIED:**
None

**** FOREIGN APPLICATIONS VERIFIED:**
JAPAN 2001-232561 07/31/2001 *OK*

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		020076
Verified and Acknowledged Examiners's initials		
TITLE : Semiconductor device and method for fabricating the same		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED		
		Total Claims		Print Claim for O.G.
		DRAWING		
ISSUE FEE		Sheets Drwg. Figs.Drwg. Print Fig.		
Amount Due	Date Paid			
		Application Examiner		
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.		

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